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# Notice of References Cited

Application No.

09/128,832

Applicant(s)

Jacobs et al.

Examiner

Rain

Group Art Unit

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Page 1 of 1

## U.S. PATENT DOCUMENTS

* A	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS
A	4,823,286	04 89	Lumelsky et al.	345	832 ✓
B	4,549,849	10 85	Louie et al.	110	3 ✓
C					
D					
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F					
G					
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* N	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
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S						
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## NON-PATENT DOCUMENTS

* U	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
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(See Manual of Patent Examining Procedure, Section 707.05(a).)

Form PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		Atty. Docket No. PHA 23,122A		Serial No. 09/128,832	
<b>INFORMATION DISCLOSURE CITATION</b> (Use several sheets if necessary)				Applicant EINO JACOBS ET AL			
				Filing Date AUGUST 4, 1998		Group 2783	
<b>U.S. PATENT DOCUMENTS</b>							
Ex. Int.		Document Number	Date	Name	Class	Sub- class	Filing Date If Approp.
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
<b>FOREIGN PATENT DOCUMENTS</b>							
		Document Number	Date	Country	Class	Sub- class	Trans. Yes No
	AG						
	AH						
	AI						
	AJ						
	AK						
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	AL	C. McDowell, "SIMAC: A Simple ALU Computer", disseration from the University of California, San Diego 1983.					
	AM						
	AN						
Examiner				Date Considered			
<b>*EXAMINER:</b> Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.							

PHN 23,122 A

Applicant

EIMO JACOBS ET AL.

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	AA	5 4 7 1 5 9 3	11/95	Branigin	395	375	
	AB	5 2 6 5 2 5 8	11/93	Fiene et al	395	725	
	AC	5 6 5 2 8 5 2	7/97	Yokota	395	384	
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	AE						
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	AG						

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	AQ	Kozuch et al., "Compression of Embedded System Programs", Proc. 1994 IEEE Inter. Conf. on computer Design: VLSI in Computers and Processors (Octo. 10-12, 1994, Cambridge, MA,) pp. 270-277.
	AR	Russ, <u>An Information-Theoretic Approach To Analysis of Computer Architectures and Compression of Instruction Memory Usage</u> , UNI Dissertation Services, Entire Book.
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Form PTO-1449  
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

Atty. Docket No.

Serial No.

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Applicant

EINO JACOBS ET AL

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	AC	5	1	7	9	6	8	9	1/12/93	COLWELL ET AL	395	425	
	AD	5	5	3	0	8	1	7	6/25/96	MASUBUCHI	395	375	
	AE	5	6	6	9	0	0	1	9/16/97	MORENO	395	706	
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	AL	Philips hopes to displace DSPs with VLIW: TriMedia processor aimed at future multimedia embedded apps. Brian cse, Microprocessor Report Vol. 8, nr 16, p. 12(4) Dec 5. 1994
	AM	Wang et al., "The Feasibility of Using Compression to Increase Memory System Performance", Proc. 2nd Int. Workshop on Modeling Analysis, and Simulation of Computer and Telecommunications Systems, pp. 107-113
	AN	Schroder et al., "Program Compression on the Instruction Systolic Array", Parallel Computing, Vol. 17, 1991, No. 2-3, pp. 207-219
	AO	Wolfe et al., "Executing Compressed Programs on An Embedded RISC Architecture", J. Computer and Software Engineering, Vol. 2, No. 3, pp. 315-27, (1994)

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Application No.

09/128,832

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Examiner

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Group Art Unit

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